

Malnad College of Engineering, Hassan
Department of Electronics and Communication
Time-Table for Semester 2024-2025 (With effect from 30/09/2024)

Semester – 3rd

Section- EC1

Class Room – SA-201, SA202*

Days	8:30 9:30	9:30 10:30	11:00 12:00	12:00 1:00	1.00 2.00	2:00 3:00	3:00 4:00	4:00 5:00	5:00 6:00
Mon	DE	M1	LICA		LUNCH	NA	AEC Lab (B3) DE Lab (B1)		DIP Maths
Tue		LICA	M1	AEC		NA	DE	M1	
Wed			AEC	NA		LICA	M1	SCR	
Thu						SCR	AEC	DE	
Fri	M1	10.00 to 12.00 AEC Lab (B1) DE Lab (B2)					NA Intg. Lab		DIP Maths
Sat	7.30 to 9.30 Yoga	10.00 to 12.00 AEC Lab (B2) DE Lab (B3)		DIP Maths					

SL. NO	Courses	Code	Faculty EC1
1.	Analog Electronic Circuits	23EC302	Mr. M N Ravikumar
2.	Digital Electronics	23EC303	Dr. D S Keerthi
3.	Network Analysis (Integrated)	23EC304	Dr. K C Deepika
4.	Linear ICs and Applications	23ESC307	Dr. Venkateswara Rao Kolli
5.	Social Connect and Responsibility	23SCR	Dr. G Padmaja Devi

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Coordinators**

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Semester – 3rd

Section- EC2

Class Room – SA-201, SA-104*

Days	8:30	9:30	11:00	12:00	1.00	2:00	3:00	4:00	5:00
	9:30	10:30	12:00	1:00	2.00	3:00	4:00	5:00	6:00
Mon					LUNCH	M1*	LICA*	NA *	DIP Maths
Tue		NA*	M1*	AEC *			NA Intg. Lab		
Wed	DE *	AEC*	M1*				AEC Lab (B5) DE Lab (B6)		
Thu	M1*	LICA *	AEC Lab (B4) DE Lab (B5)			SCR*	AEC Lab (B6) DE Lab (B4)		
Fri		NA	DE			LICA	SCR	M1	DIP Maths
Sat	7.30 to 9.30 Yoga	DE	AEC	DIP Maths					

SL. NO	Courses	Code	Faculty EC2
1.	Analog Electronic Circuits	23EC302	Dr. P B Prathap
2.	Digital Electronics	23EC303	Mrs.Sushma N
3.	Network Analysis (Integrated)	23EC304	Dr.G K Purushothama
4.	Linear ICs and Applications	23ESC307	Dr. H L Prakruthi
5.	Social Connect and Responsibility	23SCR	Dr.Yashaswini P R

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Semester -5th

Section- EC1

Class Room – SA-202, SA203[#]
SA- 104*, Seminar Hall**

Days	8:30 9:30	9:30 10:30	11:00 12:00	12:00 1:00	1.00 2.00	2:00 3:00	3:00 4:00	4:00 5:00
Mon	DC	ATW	CS	DSC/AI**	LUNCH		HDL Lab (B3)	
Tue	HDL Lab (B1)		ATW	EVS		HDL	DC Integrated Lab	
Wed			DC	HDL		CS*	HDL Lab (B2)	
Thu			HDL	IPR			CS	DSC/AI**
Fri		DC#	ATW [#]			IPR (Tutorial)		EVS
Sat			IPR	DSC/AI**				

SL. NO	Courses	Code	Faculty EC1
1	Digital System Design Using Verilog	22EC501	Dr. Venkateswara Rao Kolli
2	Control Systems	22EC502	Dr. Indira Bahaddur
3	Antenna and Wave Propagation	22EC503	Dr. P. C. Srikanth
4	Digital Communication (Integrated)	22EC504	Dr. G Murthi Mahadeva Naik
5	Professional Elective Course –I Artificial Intelligence	22EC514	Dr.G K Purushothama
6	Data Structures using C	22EC512	Mrs.Spoorthi B S
7	Research Methodology and IPR	22RIP	Dr. P. C. Srikanth
8	Environmental Studies	22EVS	Dr. G Padmaja Devi

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Semester -5th

Section- EC2

Class Room – SA-202, SA-104*,
Seminar Hall**, SA-201***, SA-203[#]

Days	8:30	9:30	11:00	12:00	1.00	2:00	3:00	4:00
	9:30	10:30	12:00	1:00	2.00	3:00	4:00	5:00
Mon	ATW [#]	EVS [#]	CS [#]	DSC*/AI**	LUNCH			
Tue		CS	HDL Lab (B5)			DC *	HDL *	ATW*
Wed	IPR (Tutorial) ***		HDL Lab (B6)				DC *	HDL *
Thu	CS****	EVS****	ATW****				IPR *	DSC*/AI**
Fri	HDL Lab (B4)		DC *	HDL *			DC Integrated Lab	
Sat			IPR *	DSC*/AI**				

SL. NO	Courses	Code	Faculty EC2
9	Digital System Design Using Verilog	22EC501	Mrs. Spoorthi B S
10	Control Systems	22EC502	Mrs. Dakshayini M R
11	Antenna and Wave Propagation	22EC503	Dr. K B Santhosh kumar
12	Digital Communication (Integrated)	22EC504	Dr. B M Hemanth Kumar
13	Professional Elective Course –I Artificial Intelligence	22EC514	Dr.G K Purushothama
14	Data Structures using C	22EC512	Dr. B M Hemanth Kumar
15	Research Methodology and IPR	22RIP	Dr. K C Deepika
16	Environmental Studies	22EVS	Mrs.Spoorthi B S

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Semester -7th

Section- EC1

Class Room – SA-203, SA-104*, SA 202**,
Seminar Hall***

Seminar Plan								
Days	8:30	9:30	11:00	12:00	1.00	2:00	3:00	4:00
	9:30	10:30	12:00	1:00	2.00	3:00	4:00	5:00
Mon		10.00 to 12.00 WCN Intg. Lab		Open Elective	LUNCH		Technical Seminar	
Tue		IPR	WCN	Open Elective			MEMS****/ IPE**	ARM
Wed		MEMS****/ IPE**	ARM	Open Elective		LP-VLSI	WCN	5G
Thu		5G****/ LP-VLSI	ARM Lab (B1)			IPR	ARM Lab (B3)	
Fri		MEMS****/ IPE**	ARM**	WCN**				
Sat		5G****/ LP-VLSI	ARM Lab (B2)					

SL. NO	Courses	Code	Faculty EC1
1	ARM Embedded System Design	21EC701	Dr. G Padmaja Devi
2	Wireless Communication and Networks (Integrated)	21EC702	Dr. D S Keerthi
3	MEMS	21EC741	Dr. C L Triveni
4	IPE		Dr. Indira Bahaddur
5	5G Technologies and Satellite Communication	21EC754	Dr. C L Triveni
6	Low Power VLSI Design	21EC752	Mr. M N Ravikumar
7	5G Technologies and Beyond	21OEEC72	Mrs. Kavana K V
8	Main Project work phase-1	21PROJ1	Dr. Yashaswini P. R
9	Technical Seminar	21SE704	Dr. B M Hemanth Kumar (2)
10	Research Methodology & Intellectual Property rights (Mandatory non-credit)	21RMIP	Mr. K A Raghuram

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Semester -7th

Section-EC2

Class Room – SA-203, SA-104*, SA 202**,
Seminar Hall***, SA201[#]

Days	8:30	9:30	11:00	12:00	1.00	2:00	3:00	4:00
	9:30	10:30	12:00	1:00	2.00	3:00	4:00	5:00
Mon		10.00 to 12.00 ARM Lab (B6)		OE	LUNCH	WCN	ARM	IPR
Tue		10.00 to 12.00 Technical Seminar		OE			MEMS/ IPE**	ARM
Wed		MEMS/ IPE**	ARM***	OE		5G**/ LP-VLSI	ARM Lab (B5)	
Thu		5G**/ LP-VLSI	WCN*				WCN Intg. Lab	
Fri		MEMS [#] / IPE**	ARM Lab (B4)					
Sat		5G**/ LP-VLSI	WCN	IPR				

SL. NO	Courses	Code	Faculty EC2
11	ARM Embedded System Design	21EC701	Mrs.Dakshayini M R
12	Wireless Communication and Networks (Integrated)	21EC702	Mrs.Manasa H S
13	MEMS	21EC741	Dr. Yashaswini P R
14	IPE		Dr. Indira Bahaddur
15	5G Technologies and Satellite Communication	21EC754	Mrs.Kavana K V
16	Low Power VLSI Design	21EC752	Mr. M N Ravikumar
17	5G Technologies and Beyond	21OEEC72	Mrs.Kavana K V
18	Main Project work phase-1	21PROJ1	Mrs. Manasa H. S
19	Technical Seminar	21SE704	Dr. P B Prathap
20	Research Methodology & Intellectual Property rights (Mandatory non-credit)	21RMIP	Dr. K B Santhosh kumar

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