

Department of Electronics Engineering (VLSI Design & Technology) Time table for the Odd Semester 2026-27										
From:07-09-2026 to 28-12-2027				III EC			Classroom: LH1			
Days/Time	8:30 9:30	9:30 10:30	10.30 11.00	11:00 12:00	12:00 1:00	1.00 2.00	2:00 3:00	3:00 4:00	4:00 5:00	5:00 6:00
Monday	Maths	DS-Verilog	Tea Break	DE	PSD	Lunch Break				
Tuesday				DS-Verilog Lab (B1)						
Wednesday		AE-I		DE	Maths		AE-I Lab		NYP (4:30 to 6:30)	
Thursday		Maths		PSD	DE			LINUX LAB		
Friday		DS-Verilog		AE-I	PSD		AT-I		Maths (Lateral entry students)	
Saturday	AE-I	DS-Verilog		DS-Verilog Lab (B2)						

Sl. No.	Course Code	Course Name		Faculty
1.	25MAVL301	Mathematics for VLSI		Mr. Pradhumna
2.	25VL302	Analog Electronics - I		Mrs. Suchithra M M
3.	25VL303	Digital System Design using Verilog		Dr. Hemanth Kumar B M
4.	25VL304	Digital Electronics		Dr. Spoorthi B S
5.	25VL305	Physics of semiconductor devices		Dr. Hemanth Kumar B M
6.	25VL306	DS-Verilog Lab	B1	Dr. Hemanth Kumar B M, Dr. Spoorthi B S
			B2	Dr. Hemanth Kumar B M
7.	25VL307A	Linux lab		Dr. Hemanth Kumar B M, Dr. Spoorthi B S, Mrs. Suchithra M M
8.	25VL302	AE-I Lab		Mrs. Suchithra M M, Dr. Spoorthi B S
9.	25NYP1	NSS, YOGA, PE		Mrs. Suchithra M M
10.		Community Project		Dr. Hemanth Kumar B M, Dr. Spoorthi B S
11	25PT1	Aptitude Training - I		

Faculty Advisor: Dr. Spoorthi B S

Signature of HoD